

Waveform Development Language (WDL)

**Using BAE's WDL Process to Extend
the Scope of the SCA closer to the Antenna**

**Prepared for:
JTRS JPO Technical Workshop
on DSP and FPGA Portability**

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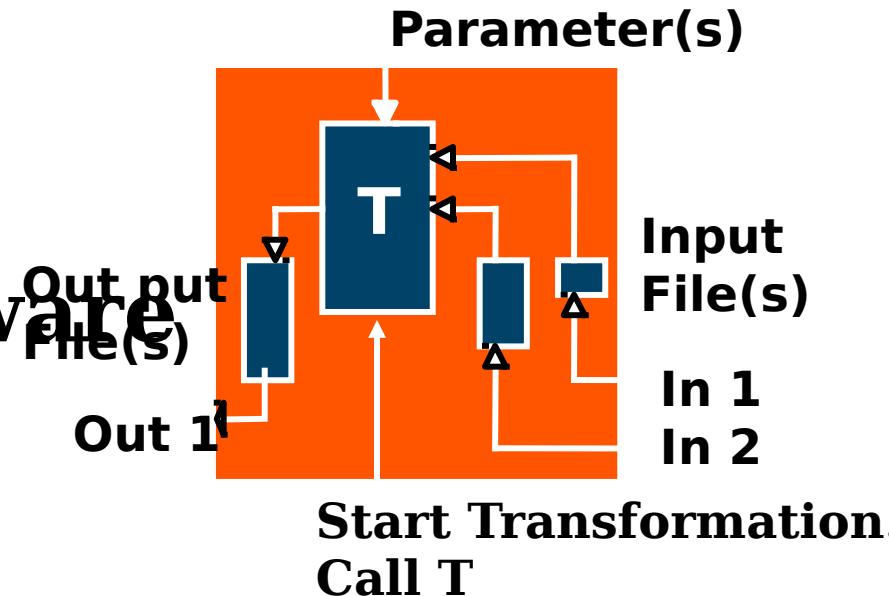
a Methodology for Software Defined Radios based on Petri Net Theory

Reuse

Modularity

**Hardware/ Software
Independence**

Portability



Using the WDL Process to extend the Scope of the SCA toward the Anten

Petri nets were introduced by C.A.Petri in the early 1960s as a **main tool for modeling distributed systems** and, in particular, notions of concurrency, non-determinism, communication and synchronization.

WDL + SCA Provides Solution to Standardization

BAE SYSTEMS

SCA Standardizes RedSide Processing

Baseband Proc'ing,

Data Link Layers

Networking and higher Layers

Expect Good Portability

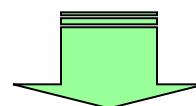
SCA Does not Standardize

Internals of Modem, Blk Proc, TSEC

Complex (Many Pieces) of Radio Code

Complexity Increasing with AJ, LPI, TSEC

Expect Poorer Portability



WDL Component-Based Design Addresses

Internals & Maximizes:

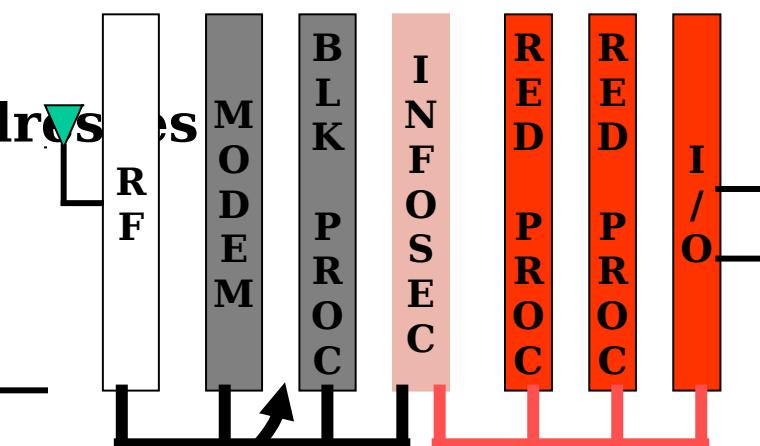
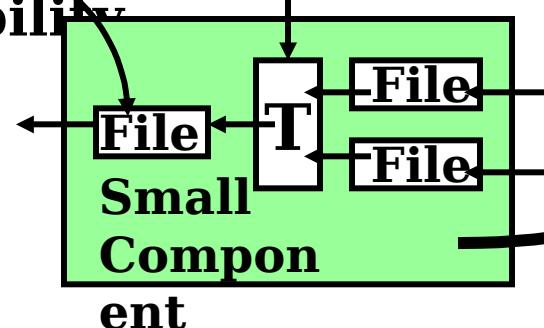
Portability

Interoperability

Reuse

Flexibility

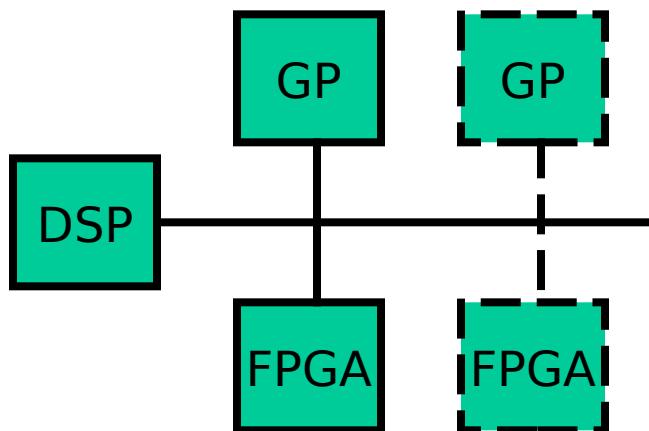
Parameters



The Portability Problem...Waveform usually

BAE SYSTEMS

Documented for Specific Architecture



Initial **Waveform Developer** usually has preconceived notion of a Specific Hardware Implementation

FPGA + DSP + GP = Waveform

{ VHF Radio
SINCGARS
UHF Radio
HQ
SATCOM
etc

VHDL	C & Assy	C, ADA = VHF Waveform
VHDL	C & Assy	C, ADA = SINCGARS
VHDL	C & Assy	C, ADA = UHF Radio
VHDL	C & Assy	C, ADA = HQII
VHDL	C & Assy	C, ADA = SATCOM/DAMA
VHDL	C & Assy	C, ADA = etc. Waveform

Major Porting effort...**Code documented at this detailed level is very difficult to understand and change.** Porting Effort Includes:

**Manual Code Translation
New Timing and Control
Massive debug & retesting**

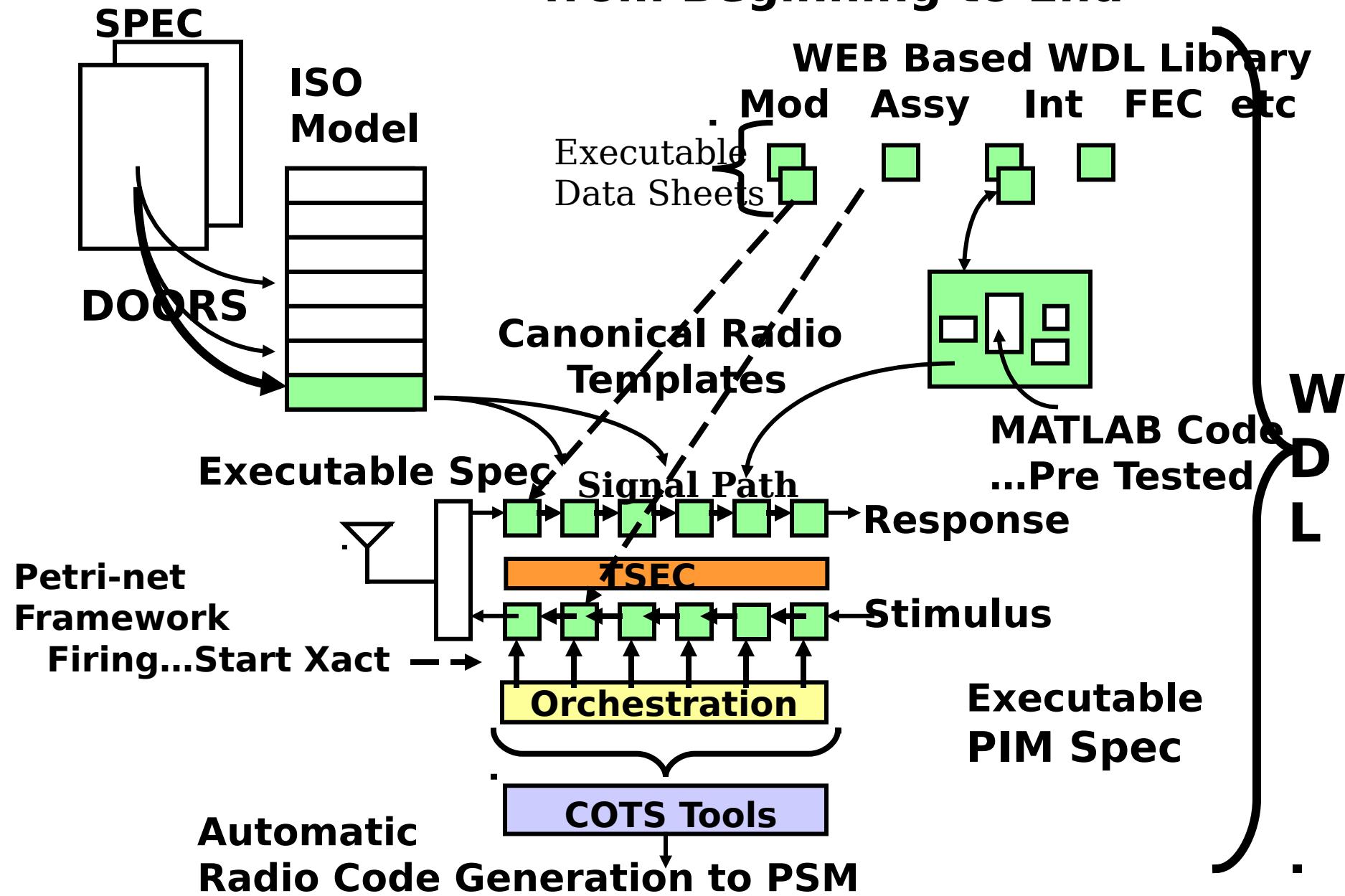


+ A new HAL

Physical Layer WDL Process...

BAE SYSTEMS

from Beginning to End



A WDL is required to achieve JTRS Waveform Portability Goals

Need a neutral executable behavioral specification language

Need way of Compiling Behavioral PIM to any Target

To combinations of GPP, DSP, FPGA, other

Need a way of reusing PIM code...make OO Radio Components

Define Classes of Components...Parameterize each component

API's defined in XML / SCA compliant

Define a Canonical Radio Template... for defining Common Radio Components

Signal Path Components...Modulators, Interleavers, Decoders

TSEC Components

Orchestration Components...Timing and Control Software

Petri Net Notation/Grammar could specify interconnection of components

Petri Net theory in existence since the 60's...very little work done

CECOM has ongoing activity in Petri Net theory